



VPC3+ Software-History

Revision 06.01.14

Liability Exclusion

We have tested the contents of this document regarding agreement with the hardware and software described. Nevertheless, there may be deviations and we do not guarantee complete agreement. The data in the document is tested periodically, however. Required corrections are included in subsequent versions. We gratefully accept suggestions for improvements.

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This document is subject to technical changes without prior notice.

1 Software History

Version	Date	Remarks
V05.00.00	08.09.2005	First release
V05.00.01	22.10.2005	<p>BugFix: Dp_cfg.h: Missing bracket: #define ASIC_USER_RAM (ASIC_RAM_LENGTH - DP_ORG_LENGTH - SAP_LENGTH)</p> <p>DP_VERSION_BUGFIX changed to 0x01</p>
V05.00.02	09.01.2006	<p>BugFix: Problem with SIEMENS PLC 318 and Diag.cfg_Fault The VPC3+/C does not set the default master addressback in the diagnostic telegram to 0xFF. Software solution in dp_user.c (named with BugFix 502).</p> <p>DP_VERSION_BUGFIX changed to 0x02</p>
V05.00.03	21.02.2006	<p>BugFix: I&M Functions: Return Codes of NRS-PDUs changed. profichip has now it's own MANUFACTURER_ID (0x01BC).</p> <p>BugFix: The definition of LITTLE_ENDIAN and BIG_ENDIAN is now correct.</p> <p>BugFix: MSAC_C2-connection: Certification Error Ifak - ILL31 transition implemented. After disconnecting profibus cable, an open MSAC-C2 connection will be closed after timeout (2*F-timer). Function acIs in dp_msac_c1 optimized. (named with BugFix 503).</p> <p>DP_VERSION_BUGFIX changed to 0x03</p>
V05.00.04	13.11.2006	<p>Certification problem (itm): The DPV1-alarms will be checked now in the function UserChkNewPrmData().</p> <p>Certification problem (procentec): The master class 2 sends an initiate.req with out data and the slave answers with next free SAP. After then the master class 2 send an initiate.req with data, the slave should now answer with the same SAP and not with new SAP. (named with BugFix 504).</p> <p>DP_VERSION_BUGFIX changed to 0x04</p>
V06.00.00	03.08.2009	<p>Supports now VPC3+S</p> <p>DP_VERSION_BUGFIX changed to 0x00</p>
V06.00.01	08.08.2009	<p>BugFix:</p> <p>DP_VERSION_BUGFIX changed to 0x01</p>
V06.00.02	17.09.2010	<p>BugFix: Error in function: VPC3_GetMasterAddress</p> <p>DP_VERSION_BUGFIX changed to 0x02</p>
V06.00.03	13.12.2010	<p>BugFix: dp_if.h: add the macro _PACKED_ to all structures Modification: Prm-, ChkCfg- and SSA-telegram via help buffer: abPrmCfgSsaHelpBuffer[HELP_BUFSIZE].</p>

		DP_VERSION_BUGFIX changed to 0x03
V06.00.04	15.02.2011	<p>BugFix: dp_if.h: DxB-macros are wrong - macro VPC3_GET_NEXT_DXB_OUT_BUFFER_CMD() modified - macro VPC3_GET_DXB_OUT_BUFFER_SM() modified modification of VPC3_Initialization(uint8_t bSlaveAddress, uint16_t wIdentNumber, psCFG psCfgData);</p> <p>DP_VERSION_BUGFIX changed to 0x04</p>
V06.00.05	19.04.2011	<p>BugFix: dp_msac2.c: MSAC_C2_InitiateReqToRes(...) - psMsgHeader->bDataLength = C2_FEATURES_SUPPORTED_2;</p> <p>Modification dp_if.h: - new macro: VPC3_SET_EMPTY_SAP_LIST() - new macro: VPC3_SET_SAP_LIST_END(OFFSET)</p> <p>Modification of following functions: - dp_if.c: VPC3_Initialization(...) - dp_if.c: VPC3_SetConstants(...) - dp_fdl.c: FDL_Init(...)</p> <p>DP_VERSION_BUGFIX changed to 0x05</p>
V06.00.06	21.10.2011	<p>BugFix: dp_msac2.c: MSAC_C2_TransmitDelay(...) If the master sends an Abort message during MSAC_C2_TransmitDelay call, the Class2 statemachine breaks down with fatal error.</p> <p>Modification of following functions: - dp_msac2.c: MSAC_C2_TransmitDelay(...) - dp_msac2.c: MSAC_C2_RealizeIndRecv(...)</p> <p>DP_VERSION_BUGFIX changed to 0x06</p>
V06.00.07	12.12.2011	<p>BugFix: - macro VPC3Write() now with cast VPC3_ADR - macro VPC3Read() now with cast VPC3_ADR If MSAC_C2 connection run out in the function MSAC_C2_Process the User-Application was informed 3 times via function UserMsac2AbortInd.</p> <p>Modification of following functions: - platform.h now without the define NOP_</p> <p>DP_VERSION_BUGFIX changed to 0x07</p>
V06.00.08	18.07.2012	<p>Modification of following functions: - dp_if.c: VPC3_MemoryTest(...)</p> <p>DP_VERSION_BUGFIX changed to 0x08</p>
V06.00.09	05.06.2013	<p>BugFix: VPC3+S in serial mode: when reading from the Control Parameter memory (address 0x000 to 0x015) only the READ_BYTE instruction may be used. Otherwise an unintended read operation to the subsequent memory location will occur leading to an unpredictable behavior of the VPC3+S.</p> <p>- dp_if.h: - macro changed: VPC3_SetDpState(_eDpState) - macro changed: VPC3_ClrDpState(_eDpState) - macro changed: VPC3_GetDpState(_eDpState)</p> <p>- dp_msac1.c: alarm statemachine</p>

		<ul style="list-style-type: none"> - AL_CheckTypeStatus() modified - AL_CheckSequenceStatus() modified - AL_CheckSendAlarm() modified <p>Modification of following functions:</p> <ul style="list-style-type: none"> - Vpc3_Initialization(...) - Vpc3_SetConstants(...) - Vpc3_Isr(...) - Vpc3_Poll(...) <p>New Feature:</p> <ul style="list-style-type: none"> - MAC Reset added to dp_isr, Dp_Appl.c <p>DP_VERSION_BUGFIX changed to 0x09</p>
V06.00.10	05.06.2013	<p>BugFix:</p> <ul style="list-style-type: none"> - Problems with PROFIBUS conformancetest: - Testcase: TC08 NMAS1, TC09 NMAS2 If the master sends two SetPrm telegrams, the software hangs in the functions DpDiag_SetPrmOk and the slave acknowledge with Prm fault. <p>Function names changed:</p> <ul style="list-style-type: none"> - DpDiag_PrmNotOk(...) --> DpDiag_SetPrmNotOk(...) - DpDiag_PrmOk(...) --> DpDiag_SetPrmOk(...) <p>Modification of following functions:</p> <ul style="list-style-type: none"> - DpDiag_SetPrmOk(...) <p>DP_VERSION_BUGFIX changed to 0x10</p>
V06.01.10	15.07.2013	<p>New function:</p> <ul style="list-style-type: none"> - support of several ident numbers <p>DP_VERSION_FUNCTION changed to 0x01</p>
V06.01.11	29.08.2013	<p>BugFix:</p> <p>I&M record set 1 .. 4</p> <ul style="list-style-type: none"> - after writing I&M 1..4 it's not allowed to read I&M 1..4. <p>DP_VERSION_BUGFIX changed to 0x11</p>
V06.01.12	02.10.2014	<p>BugFix:</p> <ul style="list-style-type: none"> - Problems with PROFIBUS conformancetest: - Testcase: TC08 NMAS1, TC09 NMAS2 If the master sends two SetPrm telegrams, the software hangs in the functions DpDiag_SetPrmOk and the slave acknowledge with Prm fault. <p>- Modification of following functions:</p> <ul style="list-style-type: none"> - DpDiag_Alarm(...) - DpDiag_SetPrmOk(...) <p>DP_VERSION_BUGFIX changed to 0x12</p>
V06.01.13	05.05.2015	<p>Modification of following function:</p> <ul style="list-style-type: none"> - VPC3_UpdateDiagnosis(..) Now: copy to diag buffer if length > 0 <p>BugFix Redundancy System:</p> <ul style="list-style-type: none"> - Modification of following function: - VPC3_InitBufferStructure(...) - set following variable to zero: <ul style="list-style-type: none"> pDpSystem->wVpc3UsedDPV0BufferMemory = 0; - AL_InitAlarm(...) - don't clear alarm buffer in Redundancy Mode

		<ul style="list-style-type: none"> - AL_AlarmEnabled(...) - AL_CheckTypeStatus(...) - remove static <p>DP_VERSION_BUGFIX changed to 0x13</p>
V06.01.14	19.11.2015	<p>BugFix:</p> <ul style="list-style-type: none"> - Modification of following pointer cast: - pDpSystem->pDxbOutBuffer1 - pDpSystem->pDxbOutBuffer2 - pDpSystem->pDxbOutBuffer3 - pDpSystem->pDoutBuffer1 - pDpSystem->pDoutBuffer2 - pDpSystem->pDoutBuffer3 - pDpSystem->pDinBuffer1 - pDpSystem->pDinBuffer2 - pDpSystem->pDinBuffer3 - pDpSystem->pDiagBuffer1 - pDpSystem->pDiagBuffer2 <p>DP_VERSION_BUGFIX changed to 0x14</p>

Table 1–1 software history

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